

| Ref # | Hits  | Search Query                             | DBs  | Default Operator | Plurals | Time Stamp       |
|-------|-------|--|--|------------------|---------|------------------|
| L1    | 10129 | memory adj3 refresh\$3                   | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2005/10/16 10:04 |
| L2    | 1191  | L1 same (control adj3 circuit)           | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2005/10/16 10:04 |
| L3    | 324   | L2 same (memory adj3 device)             | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2005/10/16 10:04 |
| L5    | 257   | 3 and "365"/\$7.ccls.                    | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2005/10/16 10:09 |
| L9    | 194   | 3 same (operation or operator\$3)        | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2005/10/16 10:08 |
| L10   | 165   | 9 and "365"/\$7.ccls.                    | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2005/10/16 10:09 |
| L11   | 157   | 9 and "365"/\$7.ccls. and @ad<"20040527" | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2005/10/16 10:09 |
| L12   | 116   | 9 and 365/222.ccls. and @ad<"20040527"   | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR               | ON      | 2005/10/16 10:15 |

|     |     |   |   |    |    |                  |
|-----|-----|---|---|----|----|------------------|
| L13 | 288 | (memory adj3 refresh adj3 circuit)<br>near5 (control adj2 circuit)      | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/10/16 10:15 |
| L14 | 3   | 13 near10 monitor\$3  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/10/16 10:17 |
| L15 | 15  | 13 same monitor\$3  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/10/16 10:51 |
| L16 | 731 | (refresh adj2 circuit) near5<br>(control adj2 circuit) near10<br>memory | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/10/16 10:52 |
| L17 | 50  | 16 near10 (refresh adj2<br>counter\$2)                                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/10/16 10:53 |
| L18 | 23  | 17 same operation   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR | ON | 2005/10/16 10:53 |